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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Application Number	09/739,349
	Filing Date	December 15, 2000
	Applicant(s)	Pieter Vorenkamp, et al.
	Group Art Unit	To be assigned 2614
	Examiner Name	To be assigned Trang Tran

U.S. PATENT DOCUMENTS						
EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TT	5,031,233	7/1991	Regan	455	307	
TT	5,715,012	2/1998	Patel et al.	348	555	
TT	5,737,035	4/1998	Rotzoll	348	725	

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS	
EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
TT	Poole, S., Surace, G., Singh, B., Dyer, N., "A CMOS Subscriber Line Audio Processing Circuit Including Adaptive Balance," IEEE International Symposium on Circuits and Systems, Finland, Espoo, Volume 2 of 3, June 7-9, 1988, pp. 1931-1934

EXAMINER SIGNATURE		DATE CONSIDERED	7/26/03
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